

09/992736  
09/992736

	Class	Subclass
ISSUE CLASSIFICATION		

CLASSIFICATION **N P**  
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**PATENT NUMBER**

## U.S. UTILITY Patent Application

O.I.P.E.	PATENT DATE
SCANNED <i>KS</i> Q.A. <i>EC</i>	


APPLICATION NO. 09/992736	CONT/PRIOR D	CLASS <del>709</del> 718	SUBCLASS 108	ART UNIT 2151 2126	EXAMINER Bulllock
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[illegible]

Rajeev Bharadhwaj

System and method for multi-level memory domain protection

PTO-2040  
12/89[illegible]

 <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input checked="" type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____	_____ (Primary Examiner) (Date)			<b>ISSUE FEE</b>	
				Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)			<b>ISSUE BATCH NUMBER</b>	

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